Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1	361/695.ccls. and (inter near integrated near circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/01 15:57
L2	249	361/695.ccls. and (integrated near circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/01 15:59
L3	14	361/695.ccls. and vga	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/01 16:01
L4	5	361/719.ccls. and vga	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/01 16:01
S1	0	702/179.ccls. and (inter near integrated near circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/01 15:56
S2	57	702/179.ccls. and (integrated near circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/23 14:45
S3	521	(component near monitoring) and (integrated near circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/23 14:46
S4	148	S3 and bus and display	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/23 15:05

S5	0	S3 and (vga near card)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/24 15:56
S6	176	S3 and card	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/23 16:28
S7	32765	(testing or monitoring) and component and circuit and card	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/23 16:29
S8	1035	S7 and vga	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/23 16:30
S9	189	S8 and temperature and fan	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/23 16:33
S10	103	(integrated near circuit) and (vga near card)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/24 10:56
S11	6	(integrated near circuit) and (vga near card) and bus and fan	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/24 10:57
S12	0	(inter near integrated near circuit) and (vga near card) and bus and fan	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/24 10:58

S13	0	(inter near integrated near circuit) and (vga near card) and bus	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/24 10:58
S14	4	(inter near integrated near circuit) and (vga near card)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/24 10:59
S15	39	345/519.ccls. and vga	US-PGPUB; USPAT; USOCR; · EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/03 16:09
S16	53	345/520.ccls. and vga	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/24 11:18
S17	405	vga and temperature and bus and (integrated adj circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/24 11:19
S18	110	S17 and fan	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/24 12:03
S19	276	(vga near card) and bus and display	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/24 13:02
S20	43	(vga near card) and bus and display and temperature	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/24 13:17

S21	16	(vga near card) and cool and fan	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/24 13:18
S22	29	(vga with card) and temperature and thermal and cooling	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/11 16:48
S23	14	vga and (inter near integrated near circuit) and display and processor and fan	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/03 16:12
S24	87	(inter near integrated near circuit) and bus and display and processor and fan	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/04 13:53
S25	2	"4903222".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/04 14:02
S26	154	detection and (inter adj integrated adj circuit) and display and processor and bus	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/04 14:52
S27	21	computer near component near monitoring	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/04 16:33
S28	2	"4683568".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/04 16:12

S29	2	"5367670".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/04 16:41
S30	2	"5225997".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/05 09:00
S31	0	detector and regulator and (temperature and frequency with GPU) and (operation adj status)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/05 09:01
S32	0	detector and regulator and (temperature and frequency with GPU)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/05 09:01
S33	0	(detector or regulator) and (temperature and frequency with GPU)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/05 09:01
S34	17	(detector or regulator) and (temperature and frequency and GPU)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/05 09:05
S35	0	regulator and temperature and frequency and GPU and bus and (inter adj integrated adj circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/05 09:06
S36	5	regulator and temperature and frequency and GPU and bus	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/05 09:20

S37	0	(GPU near temperature near monitoring) and bus and status	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/05 09:21
S38	0	(GPU near temperature near monitoring) and bus	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/05 09:21
S39	0	(GPU near temperature near monitoring)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/05 10:20
S40	2	"5485625".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/05 11:10
S41	7	(integrated near circuit near thermal near management)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/05 13:23
S42	2	"5948106".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/05 14:20
S43	2	(vga near card) and (thermal near management)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/05 16:17
S44	20	(vga near card) and cool	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/05 16:59

S45	541	(vga near card)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/05 17:00
S46	19	S45 and regulate	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/05 17:01
S47	33	S45 and temperature and frequency	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/05 17:02
S48	1	((temperature and frequency) near GPU) and monitor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/09 14:16
S49	1	((temperature and frequency) near GPU)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/09 14:16
S50	9	temperature and frequency and GPU and card	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/09 16:12
S51	2	"5767896".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/10 10:07
S52	37	detection and (inter adj integrated adj circuit) and display and processor and regulator	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/10 10:19

S53	0	(vga near controller) and (thermal near sensor) and regulator	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/10 10:20
S54	0	(vga near controller) and (thermal near sensor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/10 10:20
S55	3	(vga with controller) and (thermal near sensor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/10 10:20
S56	0	vga and fan and GPU and detector and bus and display	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/10 10:21
S57	0	vga and fan and GPU and detector and bus	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/10 10:21
S58	0	vga and fan and GPU and detector	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/10 10:21
S59	5	vga and fan and GPU	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/10 10:39
S60	1	"6549404".PN.	USPAT; USOCR	OR	OFF	2005/05/10 10:27
S61	1	"6452797".PN.	USPAT; USOCR	OR	OFF	2005/05/10 10:28
S62	1	"6157538".PN.	USPAT; USOCR	OR	OFF	2005/05/10 10:33
S63	1	"6023413".PN.	USPAT; USOCR	OR	OFF	2005/05/10 10:33

S64	1	"6157538".PN.	USPAT; USOCR	OR	OFF	2005/05/10 10:34
S65	1	"5793608".PN.	USPAT; USOCR	OR	OFF	2005/05/10 10:35
S66	1	"6452797".PN.	USPAT; USOCR	OR	OFF	2005/05/10 10:36
S67	349	vga and regulator	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/10 11:10
S68	858	(vga or agp) and regulator	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/10 11:11
S69	12	S68 and (inter adj integrated adj circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/10 11:11
S70	350	S68 and bus	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/10 11:12
S71	268	S70 and display	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/10 11:12
S72	1	(vga adj card) and gpu and bus and fan	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/23 15:50
S73	2	(vga adj card) and gpu and bus	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/23 15:53

S74	3	(vga adj card) and gpu and fan	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/23 15:54
S75	3	vga and card and gpu and fan and bus	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/23 15:55
S76	1	vga and gpu and thermal and management	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/24 15:28
S77	0	vga and gpu and (component near detection)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/24 14:39
S78	1	vga and gpu and (component with detection)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/24 14:39
S80	5	vga and agp and cool	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/24 15:57
S81	1342	(monitoring adj device) and detection and bus and display and processor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/25 15:35
S82	11	S81 and (inter adj integrated adj circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/25 15:36

S83	38	S81 and (vga or gpu)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/25 15:47
S84	155	detection and (inter adj integrated adj circuit) and bus and display and processor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/25 15:55
S85	21	vga and gpu	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/25 16:05
S86	9	(device near monitoring) and (inter near integrated near circuit) and regulator	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/01 09:35
S87	652	(device near monitoring) and regulator and bus	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/01 09:36
S88	346	S87 and display and detection	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/01 11:03
S10 1	2	"20030220721"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/01 11:06
S10 2	2	"6760649".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/01 11:49

S10 3	2	"6134667".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/01 11:25
S10 4	2	"6006168".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/01 11:25
S10 5	0	700/301.ccls. and (inter near integrated near circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/01 11:28
S10 6	12	"700"/$.ccls. and (inter near integrated near circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/01 11:28
S10 7	101	"713"/$.ccls. and (inter near integrated near circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/01 11:28
S10 8	1	"6760649".uref.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/01 11:41
S10 9	2308	detection and bus and processor and display and regulator	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/01 11:49
S11 0	35	S109 and (inter adj integrated adj circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/01 12:04

S11 1	2	((vga or agp or gpu) near cool)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/01 12:07
S11 2	3	((vga or agp or gpu) near thermal)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/01 12:07
S11 3	22	((vga or agp or gpu) near temperature)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/01 12:08
S11 4	143	(vga or agp or gpu) and regulate and processor and display and detection and bus	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/01 12:59
S11 5	38	(thermal adj management) and regulator and detection and bus and processor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/01 14:05
S11 6	65	microbus and system	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/01 13:44
S11 7	7	(thermal adj management) and (graphic near cards)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/01 14:05